



Receipt

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Raymond J. Beffa

Serial No.: 10/092,185

Filed: March 6, 2002

**For: METHOD FOR SORTING
INTEGRATED CIRCUIT DEVICES**

Examiner: Unknown

Group Art Unit: 3653

**Attorney Docket No.: 3037.10US
(95-1074.10)**

CERTIFICATE OF MAILING

I hereby certify that this paper or fee along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail (under 37 C.F.R. § 1.8(a)) on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, Washington, D.C. 20231.

April 3, 2002
Date of Deposit

Signature of registered practitioner or other person having reasonable basis to expect mailing to occur on date of deposit shown pursuant to 37 C.F.R. § 1.8(a)(1)(ii)

Dendra Pfeil
Typed/printed name of person whose signature is contained above

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GROUP 3600

REQUEST FOR CORRECTED FILING RECEIPT

Application Processing Division
Customer Correction Branch
Commissioner for Patents
Washington, D.C. 20231

Sir:

Attached is a copy of the official filing receipt received from the PTO in the above application. Issuance of a corrected filing receipt in accordance with the annotations on the attached filing receipt copy is respectfully requested.

The errors to be corrected are as follows:

In the Domestic Priority Data as Claimed by Applicant

Please delete the last two lines

"AND SAID 09/941,892 08/28/2001

IS A DIV OF 09/133,336 08/13/1998 PAT 6,147,316 "

Serial No. 10/092,185

These corrections are not due to any errors by applicant and no fee is due.

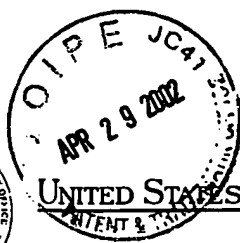
Respectfully submitted,

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Date: April 3, 2002

JRD/sls:djp

Enclosure: Copy of Official Filing Receipt with annotations correcting errors



UNITED STATES PATENT AND TRADEMARK OFFICE

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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/092,185	03/06/2002	3653	740	3037.10US (95-1074.10)	8	3	3

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P.O. BOX 2550
SALT LAKE CITY, UT 84110

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CONFIRMATION NO. 1655

FILING RECEIPT



OC000000007749668

Date Mailed: 03/29/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Raymond J. Beffa, Boise, ID;

Domestic Priority data as claimed by applicant

THIS APPLICATION IS A CON OF 09/941,092 08/28/2001 PAT 6,373,011
WHICH IS A CON OF 09/713,912 11/15/2000 PAT 6,365,861
WHICH IS A DIV OF 09/520,067 03/07/2000 PAT 6,350,959
WHICH IS A CON OF 09/133,338 08/13/1998 PAT 6,100,486
WHICH IS A DIV OF 08/785,353 01/17/1997 PAT 5,927,512
~~AND SAID 09/941,092 08/28/2001~~
~~IS A DIV OF 09/133,336 08/13/1998 PAT 6,147,316~~

Foreign Applications

If Required, Foreign Filing License Granted 03/29/2002

Projected Publication Date: 07/11/2002

Non-Publication Request: No

Early Publication Request: No

Title

Method for sorting integrated circuit devices

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GROUP 3600

Preliminary Class

209

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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Bib Data Sheet

CONFIRMATION NO. 1655

SERIAL NUMBER	FILING DATE	CLASS	GROUP ART UNIT	ATTORNEY DOCKET NO.
10/092,185	03/06/2002 RULE	209	3653	3037.10US (95-1074.10)

APPLICANTS

Raymond J. Beffa, Boise, ID;

** CONTINUING DATA *****

THIS APPLICATION IS A CON OF 09/941,092 08/28/2001 PAT 6,373,011
WHICH IS A CON OF 09/713,912 11/15/2000 PAT 6,365,861
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WHICH IS A DIV OF 08/785,353 01/17/1997 PAT 5,927,512

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GROUP 3300

** FOREIGN APPLICATIONS *****

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 03/29/2002

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY ID	SHEETS DRAWING 8	TOTAL CLAIMS 3	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature Initials				

ADDRESS

24247

TITLE

Method for sorting integrated circuit devices

FILING FEE RECEIVED 740	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees
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		<input type="checkbox"/> 1.17 Fees (Processing Ext. of time)
		<input type="checkbox"/> 1.18 Fees (Issue)
		<input type="checkbox"/> Other _____
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